Form PTO 1449 (Modified)	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY DOCKET NO. 243570US-2 DIV	SERIAL NO. 10/684,438 NEW APPLICATION
LIST OF REFERENCES CITED BY APPLICANT		APPLICANT Tomoaki YABE	
		FILING DATE ~~HEREWITH~~ 10/15/03	GROUP 2816

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
TDC	AA	6,313,694	11/06/01	SOHN	327	541	
TDC	AB	6,184,744	02/06/01	MORISHITA	327	541	
TDC	AC	6,064,188	05/16/00	TAKASHIMA ET AL.	323	316	
TDC	AD	5,554,953	09/10/96	SHIBAYAMA ET AL.	327	541	
TDC	AE	5,280,455	01/18/94	KANAISHI	365	229	
TDC	AF	5,493,234	02/20/96	OH	327	541	
TDC	AG	5,352,935	10/04/94	YAMAMURA ET AL.	327	540	
TDC	AH	5,249,155	09/28/93	ARIMOTO ET AL.	365	222	
TDC	AI	4,683,382	07/28/87	SAKURAI ET AL.	327	544	
~~TDC~~	AJ						
	AK						
	AL						
	AM						
	AN						

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	TRANSLATION YES	TRANSLATION NO
	AO					
	AP					
	AQ					
	AR					
	AS					
	AT					
	AU					
	AV					

OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.)

TDC	AW	Takayuki OOTANI, et al. "A 4-Mb CMOS SRAM WITH A PMOS THIN-FILM-TRANSISTOR LOAD CELL" IEEE Journal of Solid-State Circuits, Vol. 25, No. 5, October 1990, pgs. 1082-1092
	AX	
	AY	
	AZ	

☐ Additional References sheet(s) attached

Examiner T. Cunningham — Date Considered 10/13/04

*Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.